

FIG. 1

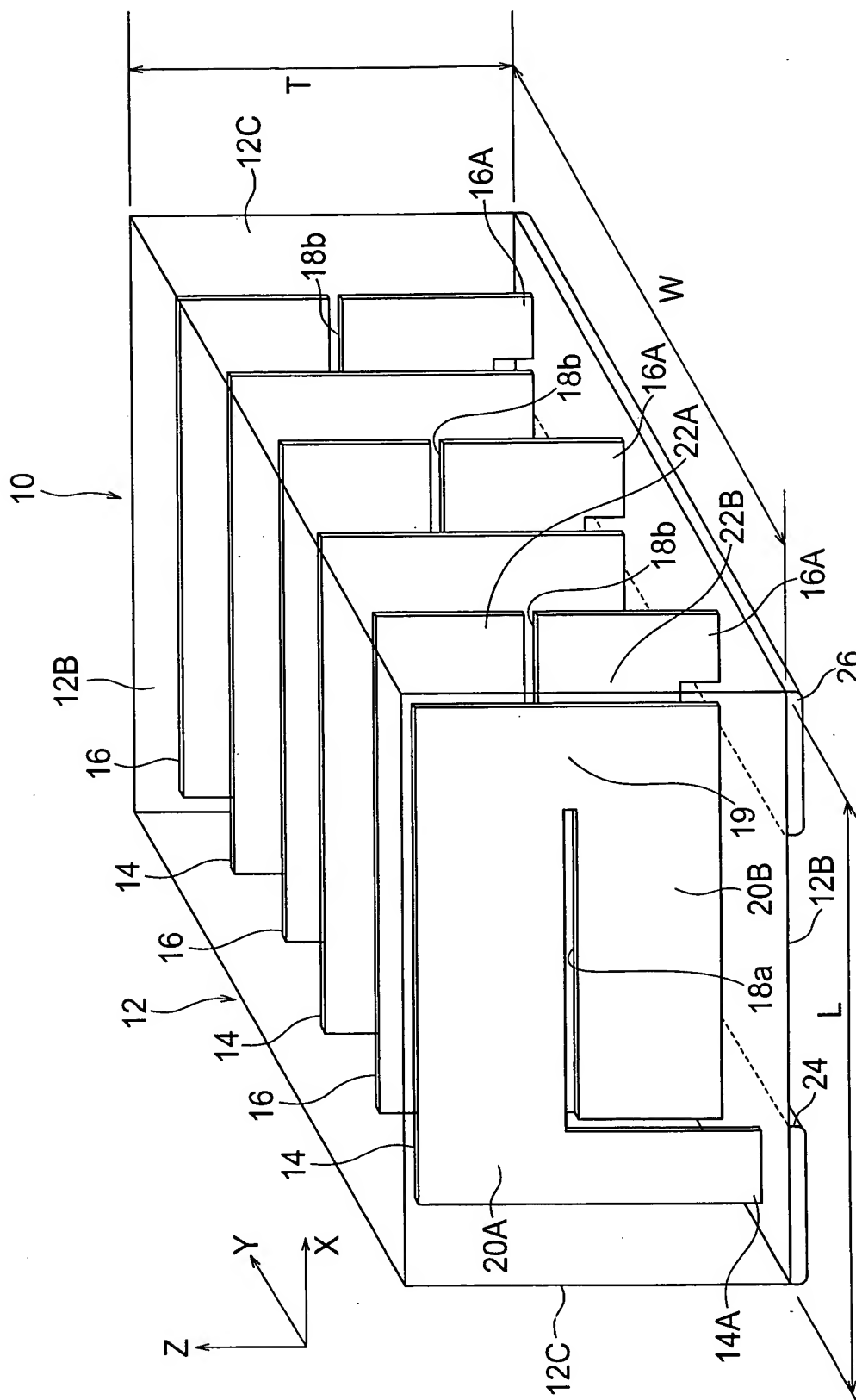


FIG. 2

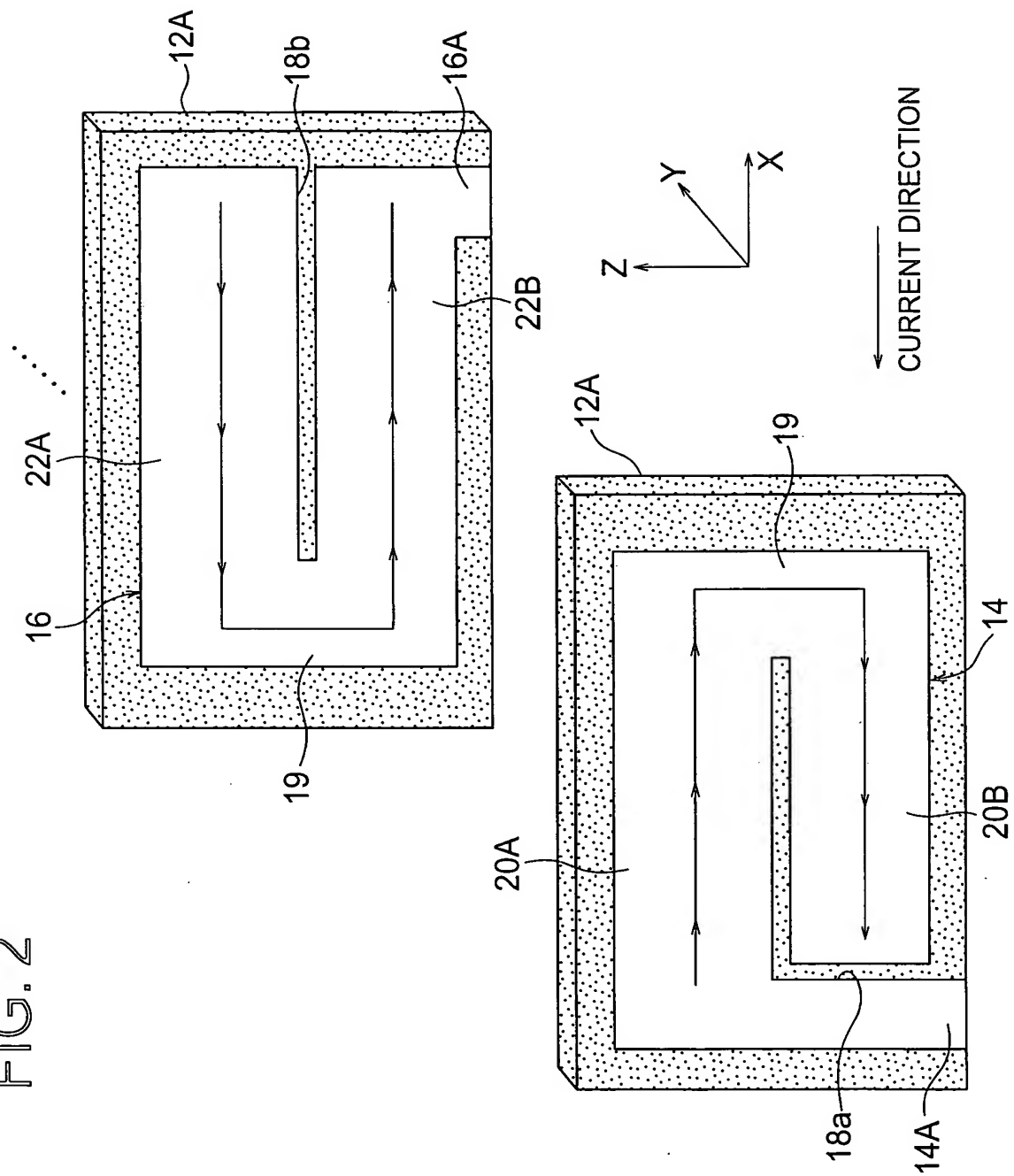


FIG. 3

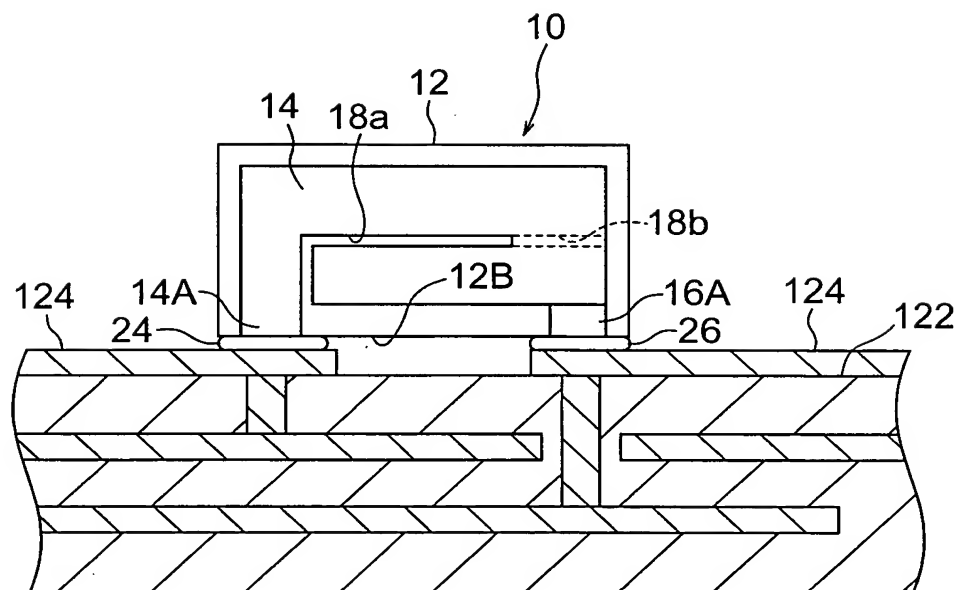


FIG. 4

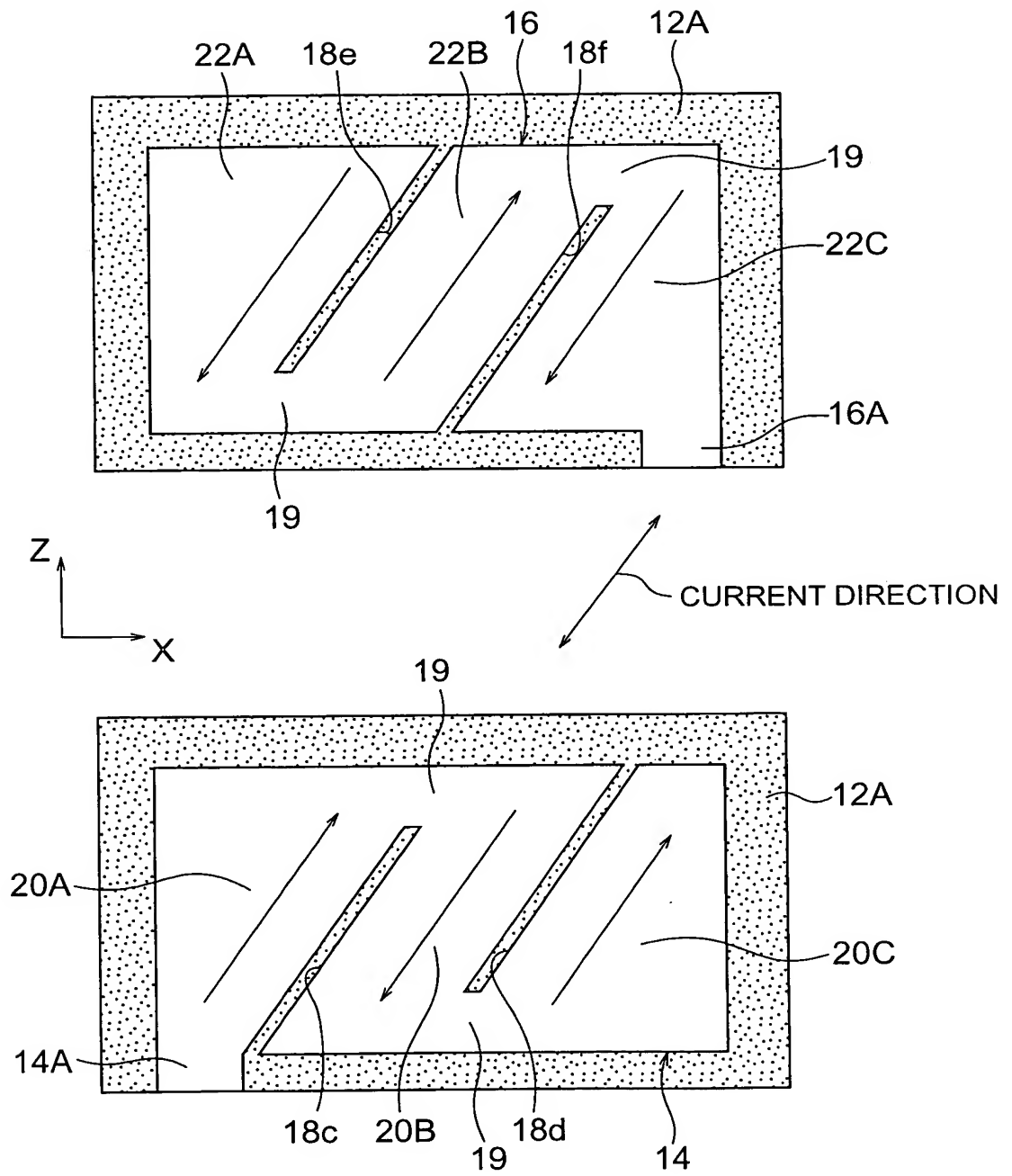


FIG. 5

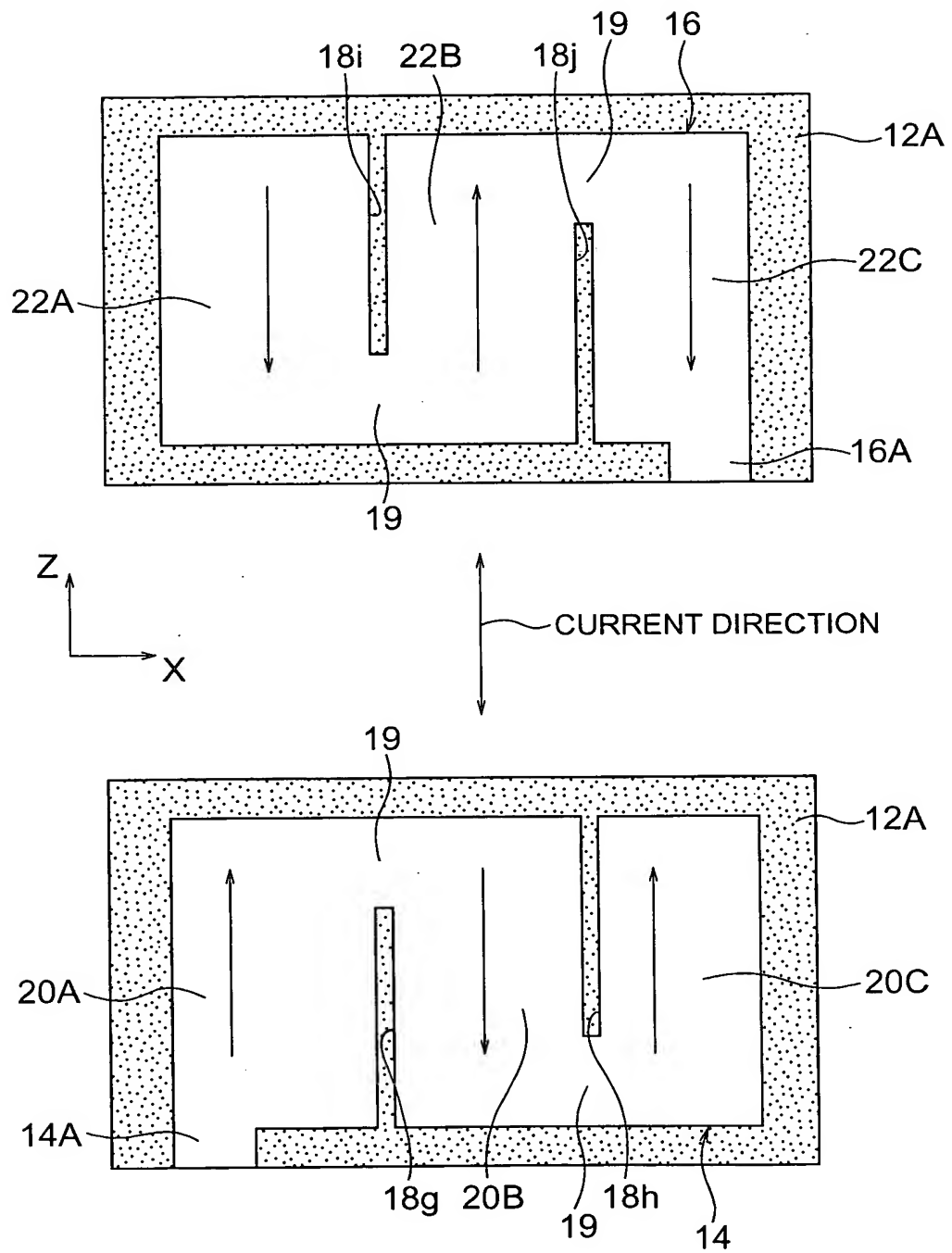


FIG. 6

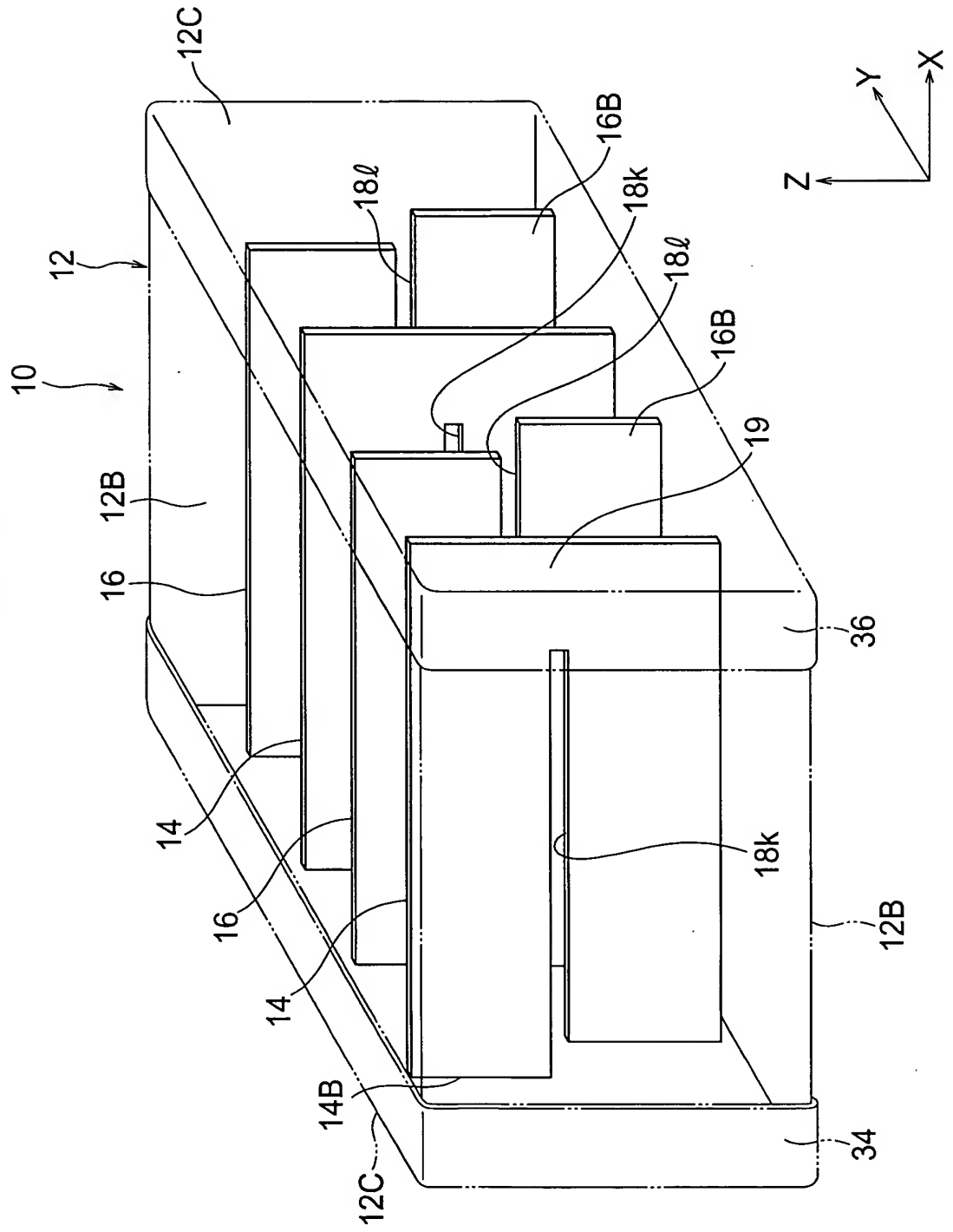


FIG. 7

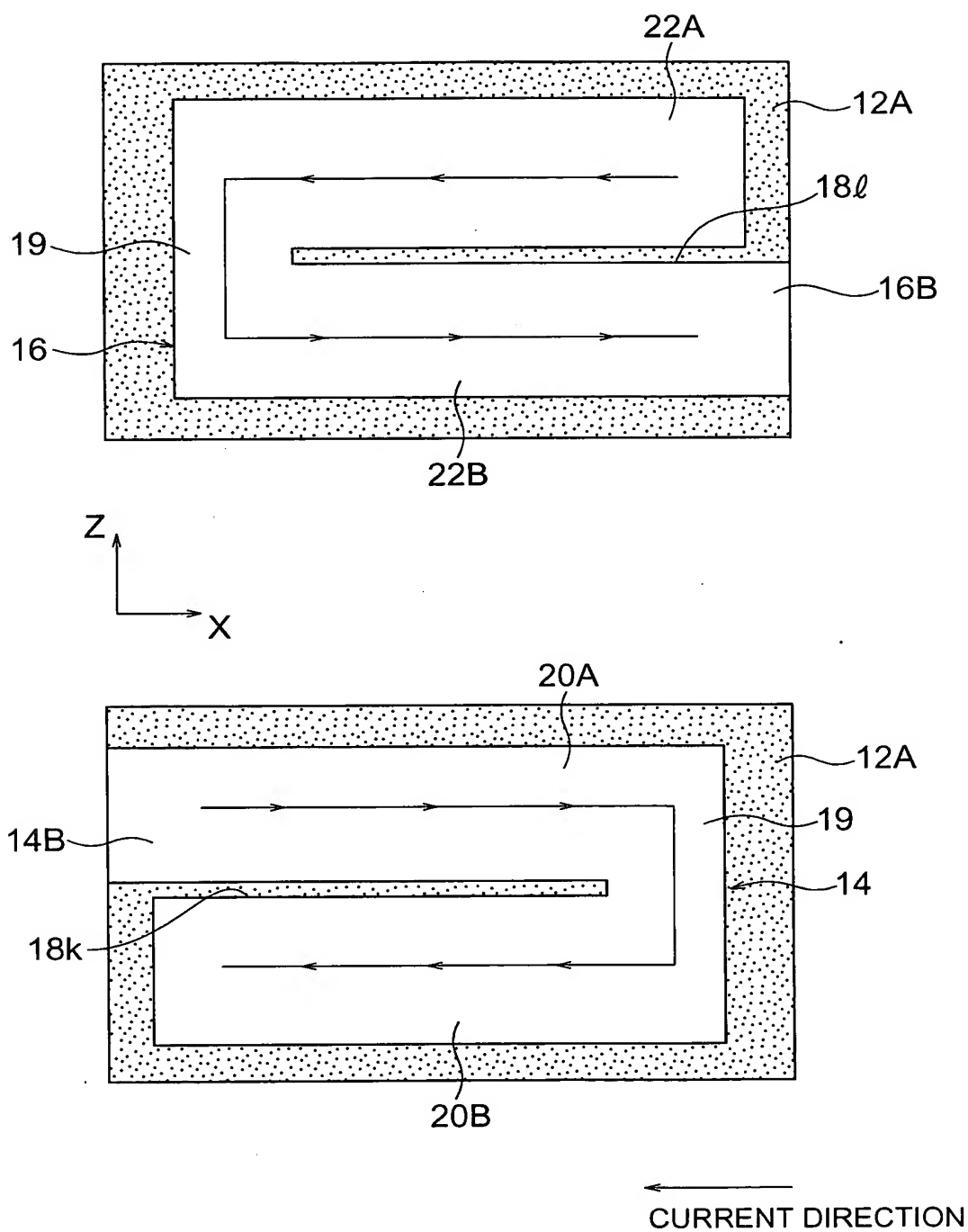


FIG. 8

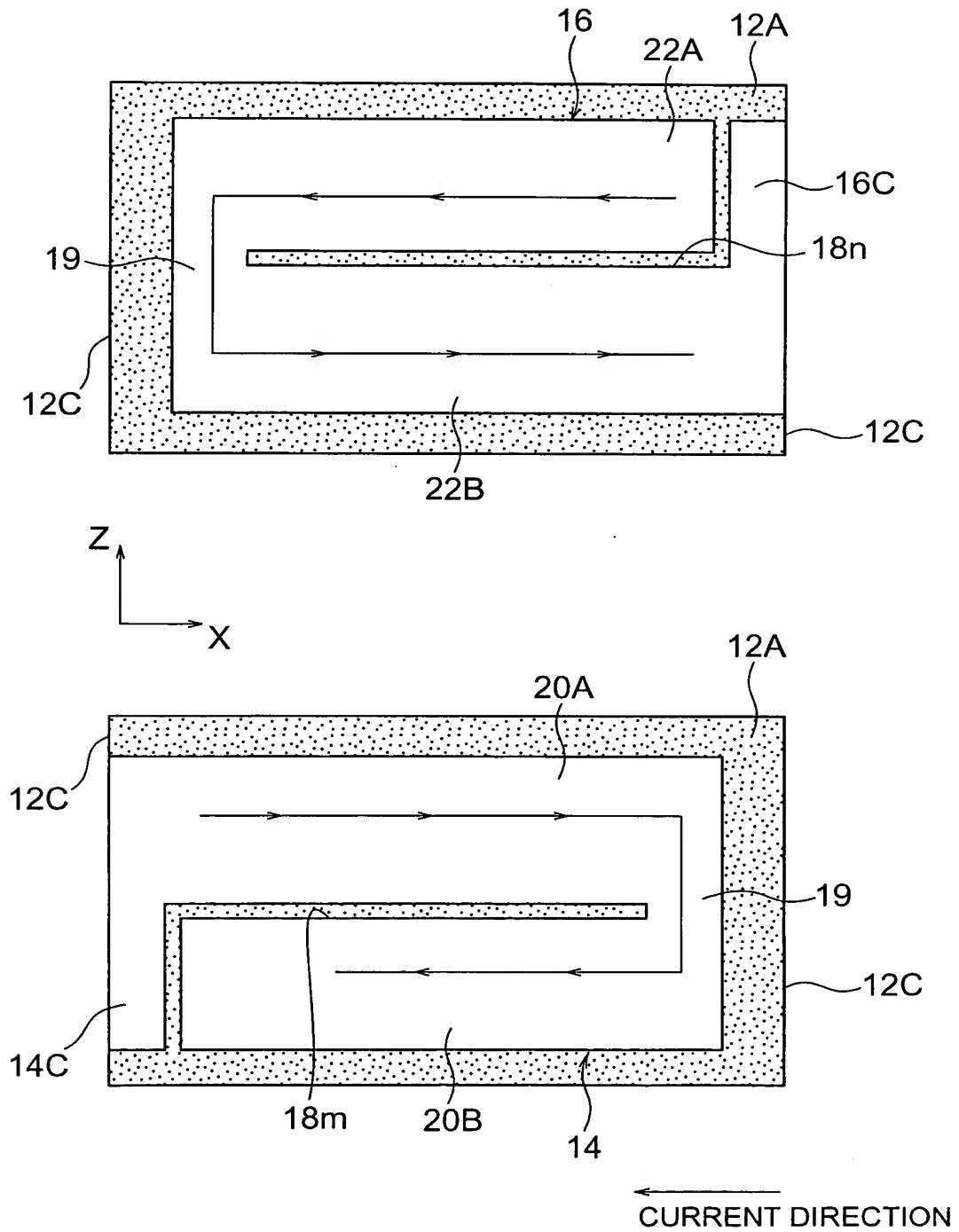


FIG. 9

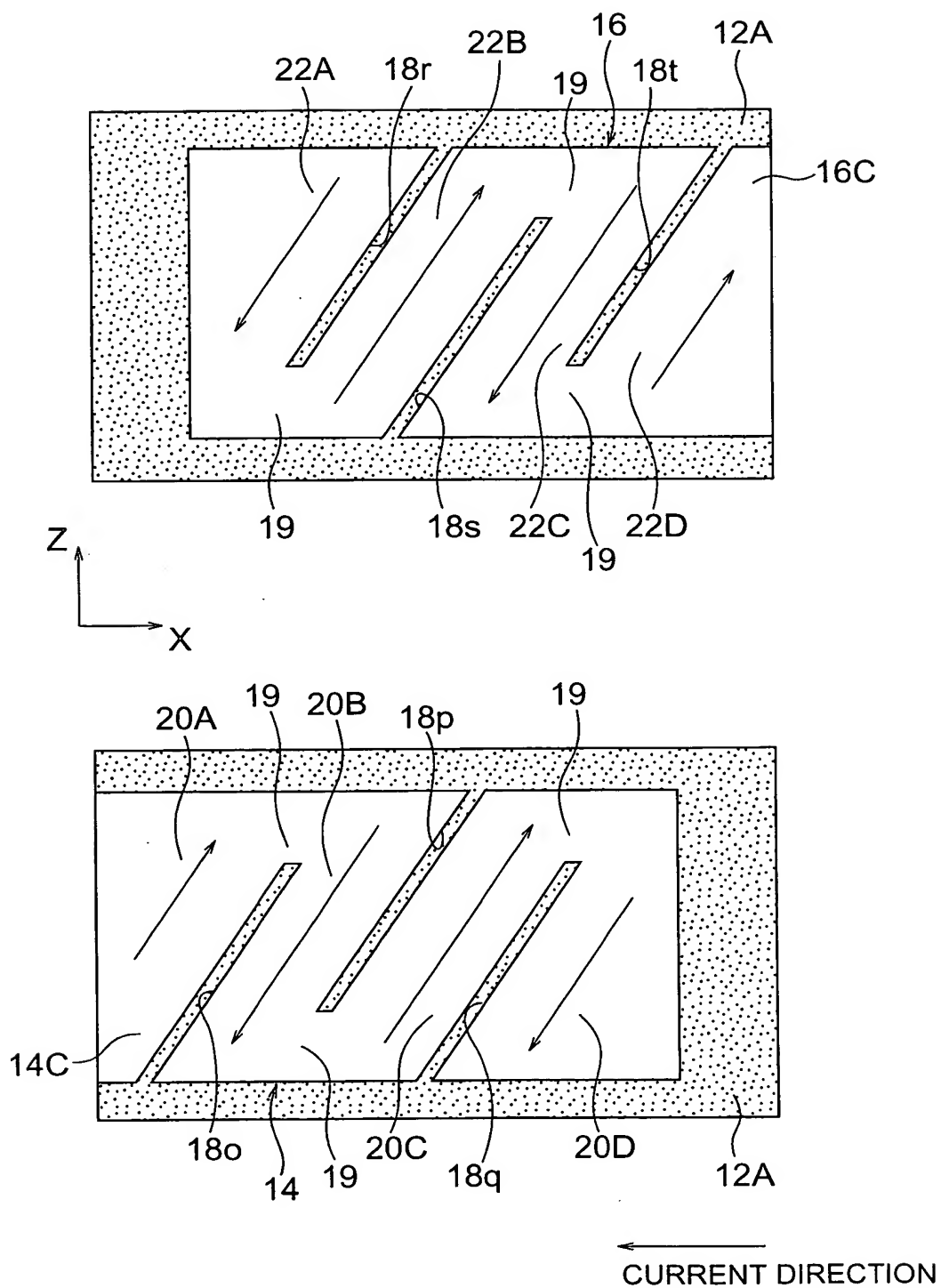


FIG. 10

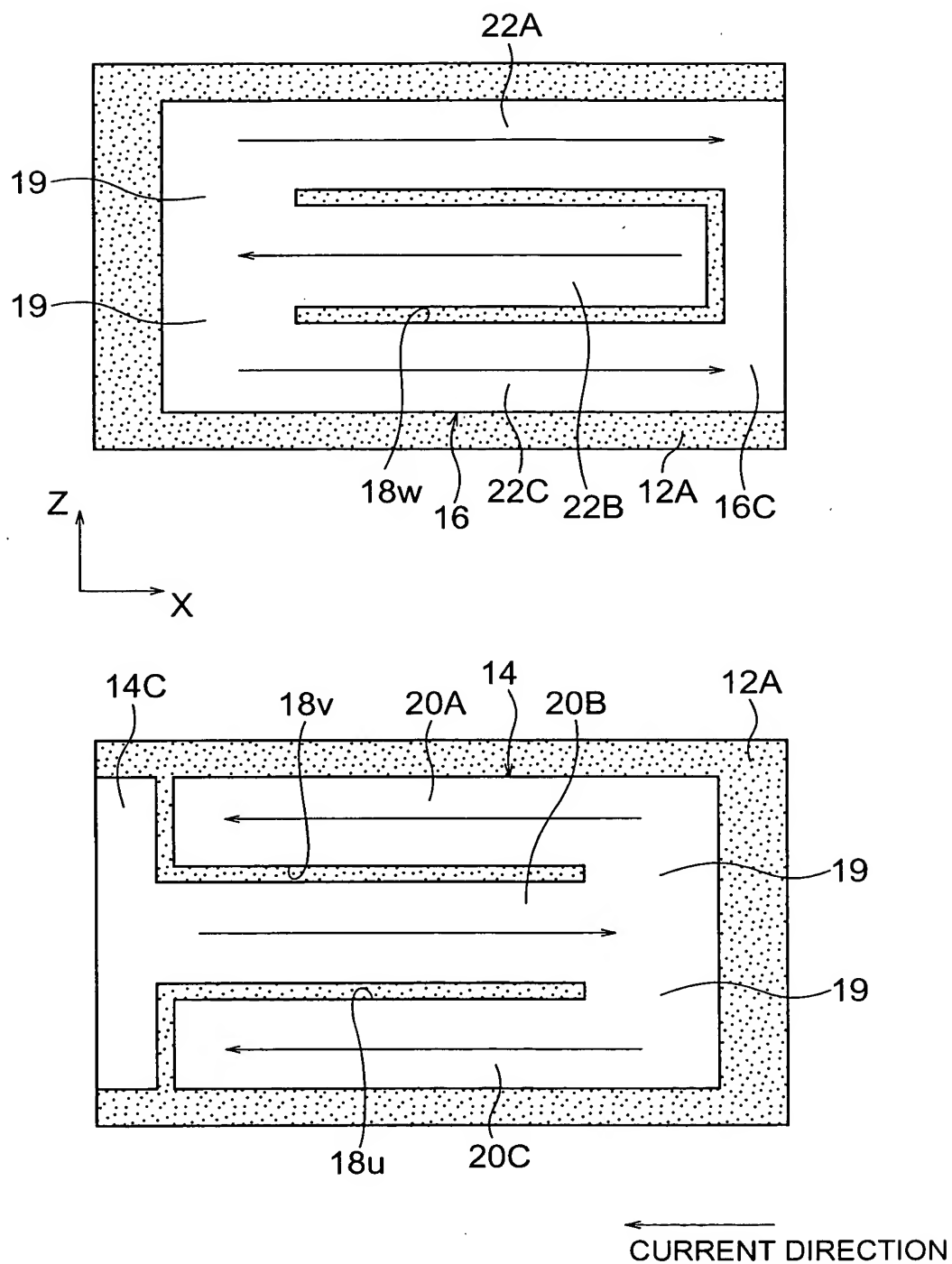


FIG. 11

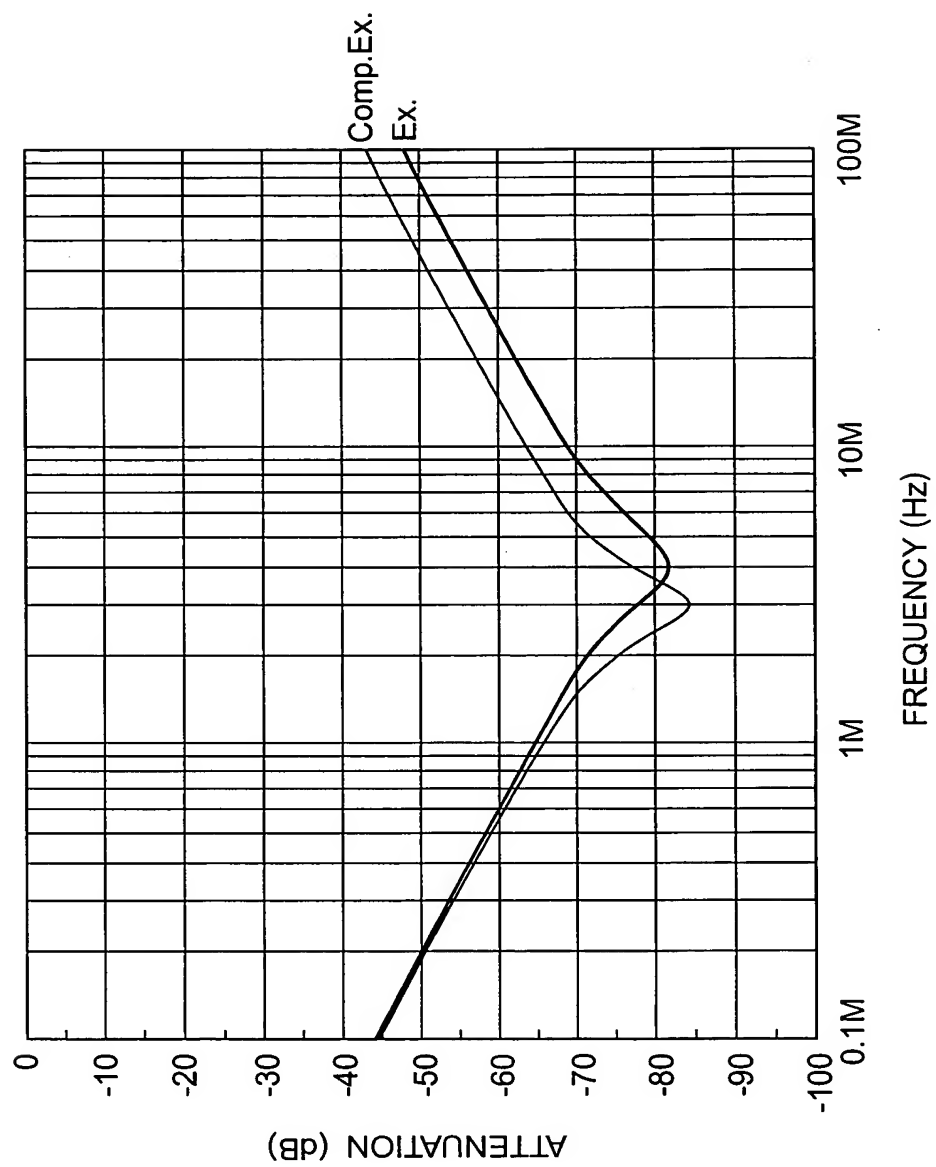
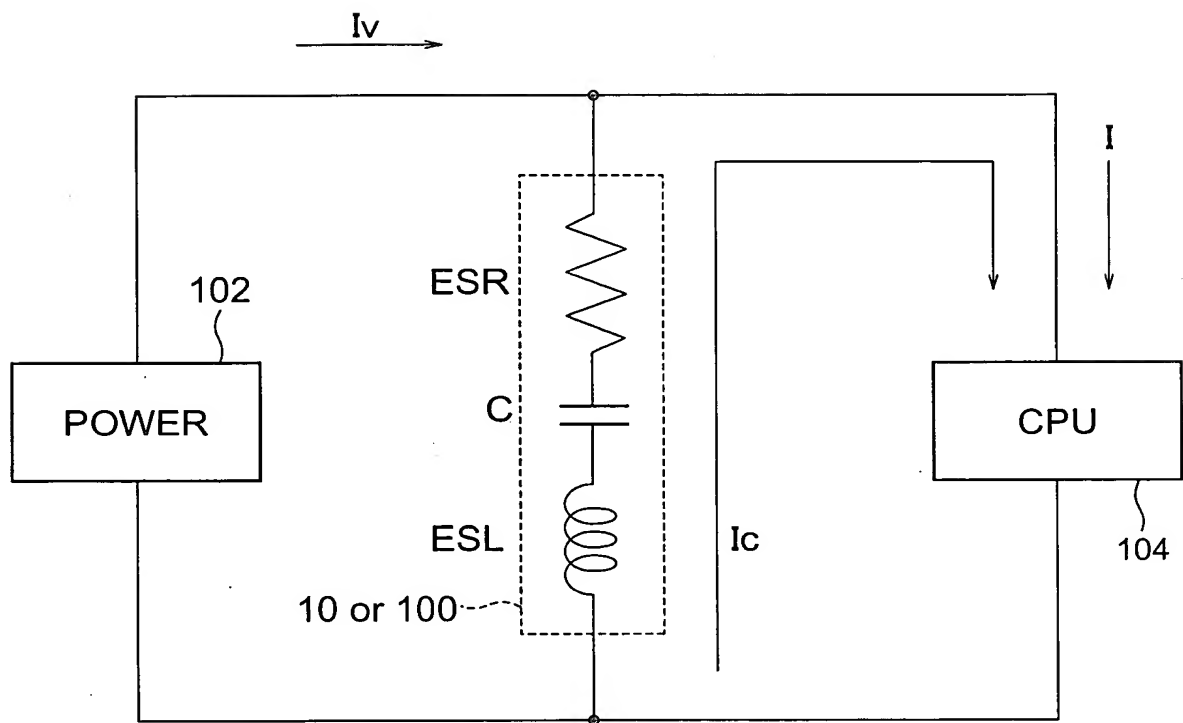


FIG. 12



I : DRIVE CURRENT OF CPU
 I_c : DISCHARGE CURRENT FROM CAPACITOR
 I_v : CURRENT FROM POWER SOURCE
 C : ELECTROSTATIC CAPACITY
 ESR : EQUIVALENT SERIAL RESISTANCE
 ESL : EQUIVALENT SERIAL INDUCTANCE

FIG. 13
PRIOR ART

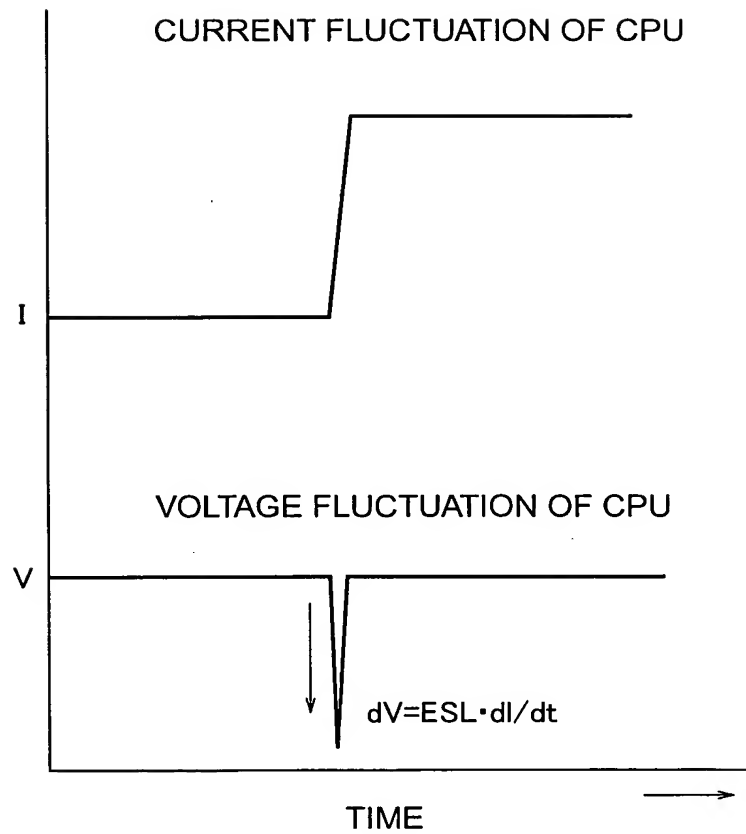


FIG. 14
PRIOR ART

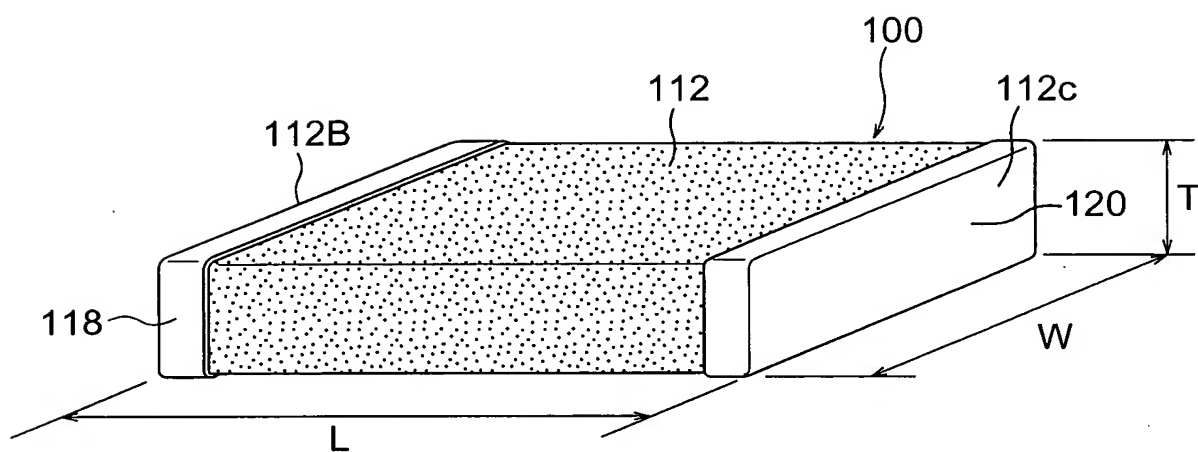


FIG. 15
PRIOR ART

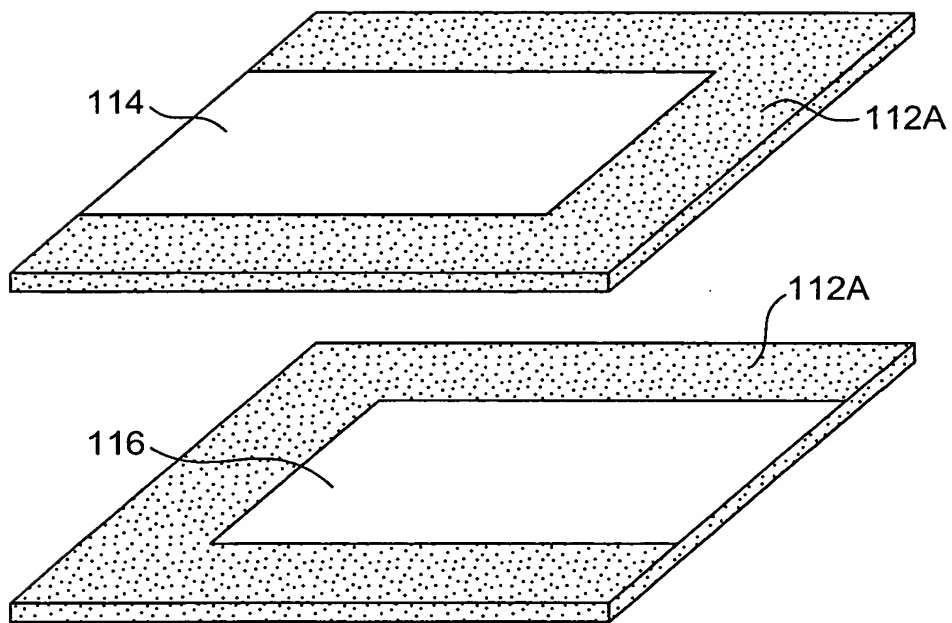


FIG. 16
PRIOR ART

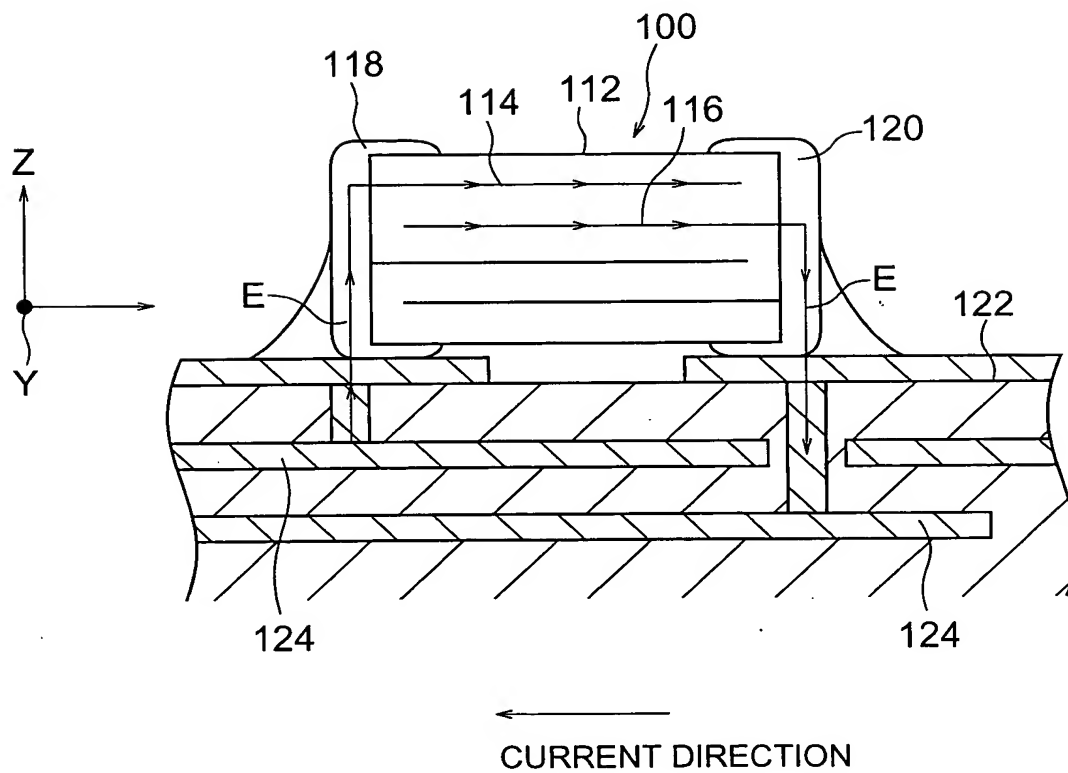


FIG. 17
PRIOR ART

